

**Job Title:** MANAGER, CORE PROCESSOR DEVELOPMENT

**Employment Type:** Full Time

**Location:** Hybrid/Barcelona, Spain

**Department:** VLSI Engineering

**Description:** We are looking for a digital design manager to drive a team in charge of high-speed digital design for a RISC-V-based processor. This role involves leading world-class ASIC work on advanced process technologies.

**Responsibilities:**

- Own and assist in the development of key modules in a high-performance system for AI applications
- Work closely with all parts of engineering to coordinate project execution and meet product specifications: Architecture, Verification, Physical Design, Post-Silicon Bring Up, and Program Management
- Accurately predict schedules, track team progress, and optimize resource allocation
- Supervise and train junior engineers (based on relevant years of experience)
- Participate in internal reviews to ensure adherence to the company's design processes and guidelines, and contribute to improving those processes
- Support the recruitment of new talent with the required skills

**Qualifications:**

- MS in Electrical Engineering or equivalent
- 10+ years of experience managing design teams
- 15+ years of ASIC design experience
- Experience with RISC-V-based systems or similar
- Strong knowledge of core processor sub-systems and concepts, including pipelines, data and instruction caches, arithmetic units (fixed and floating-point), and scoreboards
- Expertise in ASIC and/or FPGA design methodology, including Verilog, front-end design, simulation, synthesis, timing, power analysis, and verification CAD tools
- Excellent verbal and written communication skills
- Natural leadership abilities